



DOCUMENT CHANGE REQUEST

DCR number	1476	Changes required for:	General	Originator:	Stephan Hernandez
Date:	2021/12/01	Date sent:	2021/12/01	Organisation:	ESTEC
Status:	IMPLEMENTED				

Title: REP007 European Preferred Parts List

Number:	REP007	Issue:	42
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Other documents affected:

Page:

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Paragraph:

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Original wording:

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Proposed wording:

New entries:

Microchip / F, EPPL part 1
Single port gigabit Ethernet copper PHY with GMII/RGMII/MII/RMII interfaces based on type VSC8541RT (ESCC 9405/020)

TI / USA, EPPL part 1 (MIL QML)
Ultra low-Noise JESD204B compliant clock jitter cleaner, LMK04832-SP

TI / USA, EPPL part 1 (MIL QML)
-15-V to 65-V Common Mode Unidirectional Current-Shunt Monitor, INA901-SP


TI / USA, EPPL part 1 (MIL QML)
Linear Analog to Digital converter 12-bit, dual 3.2-GSPS or single 6.4-GSPS, ADC12DJ3200QML-SP

RD ALFA Microelectronics / Latvia, EPPL part 2
Low power Bipolar Linear Operational Amplifier, RD124A

UMS / France, EPPL part 2
0.15µm GaN HEMT MMIC process for High Power amplifiers up to 35GHz (AlGaIn/GaN on SiC)

Justification:

Publication EPPL 43

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
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