



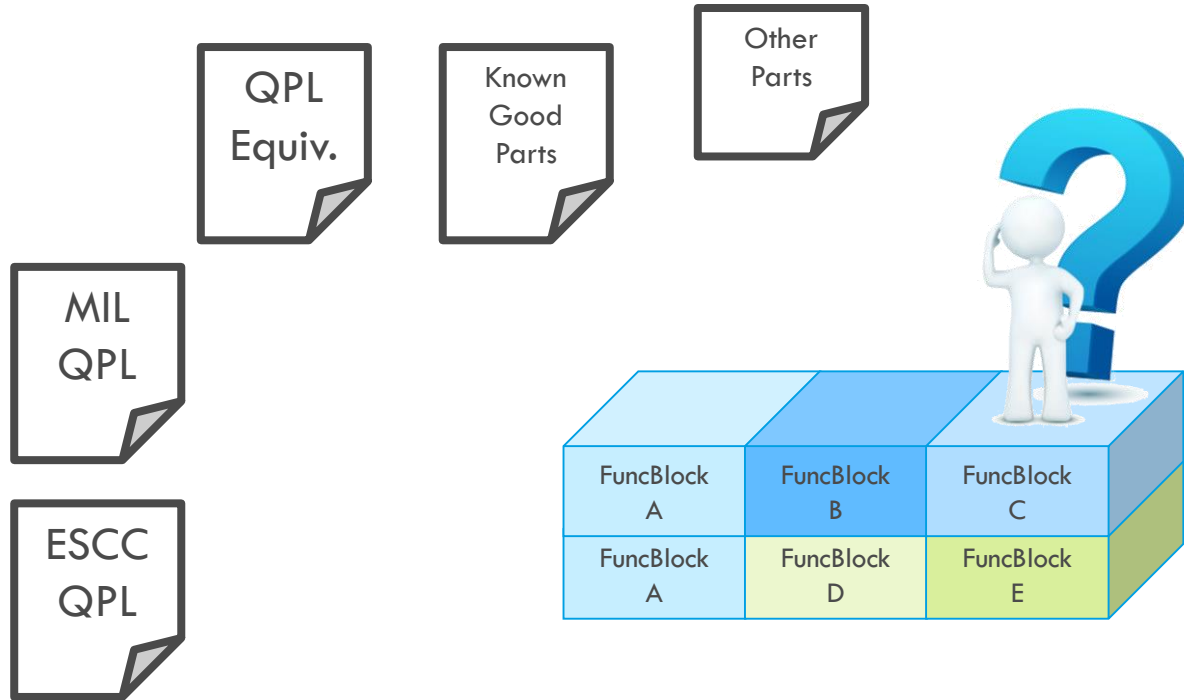
## SPACE EQUIPMENT WITH COTS PARTS - FROM DESIGN TO PRODUCT

Dr. Jens Haala, TESAT Spacecom

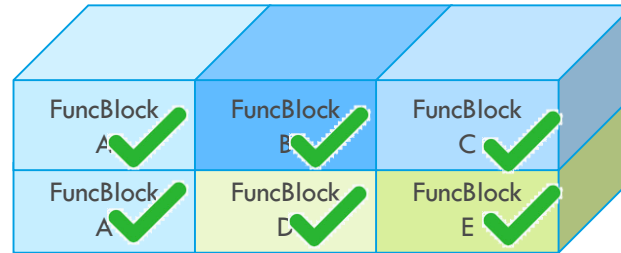


- » Equipment design with Classic Space
  
- » Equipment design with COTS
  - » Challenges
  - » Risk
  
- » Mitigation of Risks

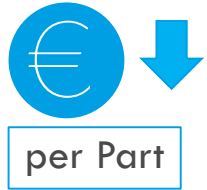
## The Classic Way



# The Old Way

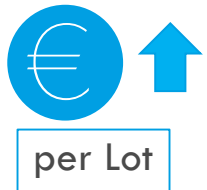


 **BUILD**



Lower Quality Level (less Screening/LAT)

- » Automotive
- » Industrial

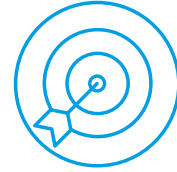


Typically Additional Tests needed

- » Lot Acceptance
- » Radiation
- » Single Event Effect
- » Screening

Consequences:

- » High Volume
- » Big Lot Size
- » Small Variety



Risks:

- » Traceability
- » Tests may fail → Part not flight-worthy
- » Waste of Money



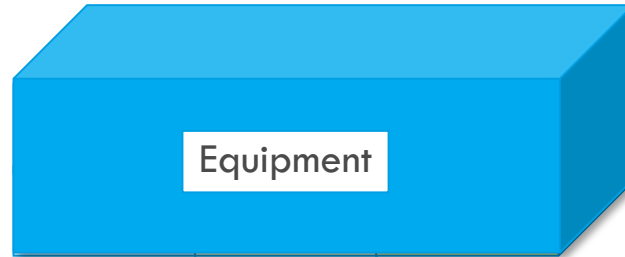
Pref.  
Parts List

**Limit to a Minimum!**

Vendors

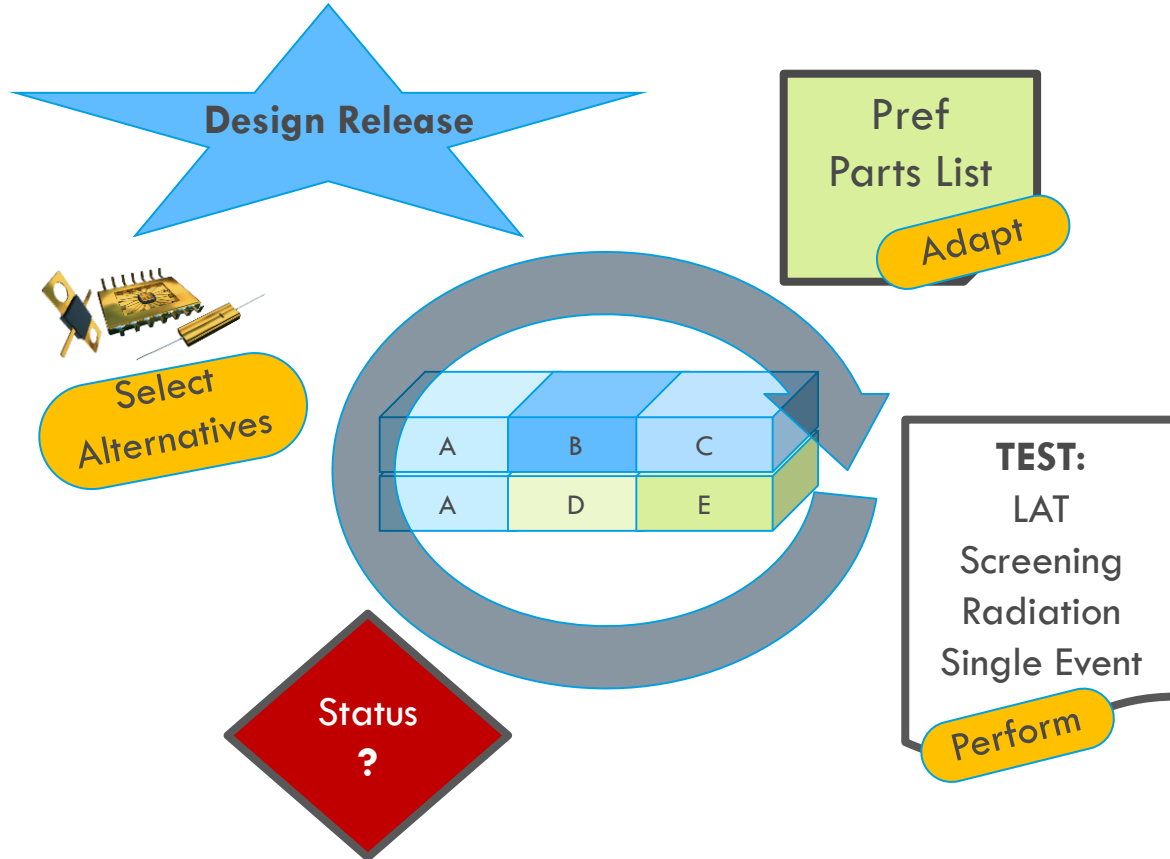
Automotive

Industrial



- » Equipment function is given
- » Identify building blocks
- » Identify parts to create building block by function
- » Collect part candidates information
- » Pre-selection

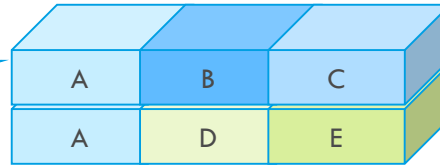




- » Equipment function is given
- » Identify building blocks
- » Identify parts to create building block by function
- » Collect part candidates information
- » Pre-selection
- » Design release
- » Adapt pre-selection
- » Identify missing screening/LAT tests
- » perform delta tests
- » perform radiation test
- » check status
- » propose alternatives
- » Design release...



**Design Release**



- » Thermal cycling
- » Radiation analysis
- » Design Review

**No Surprise!**

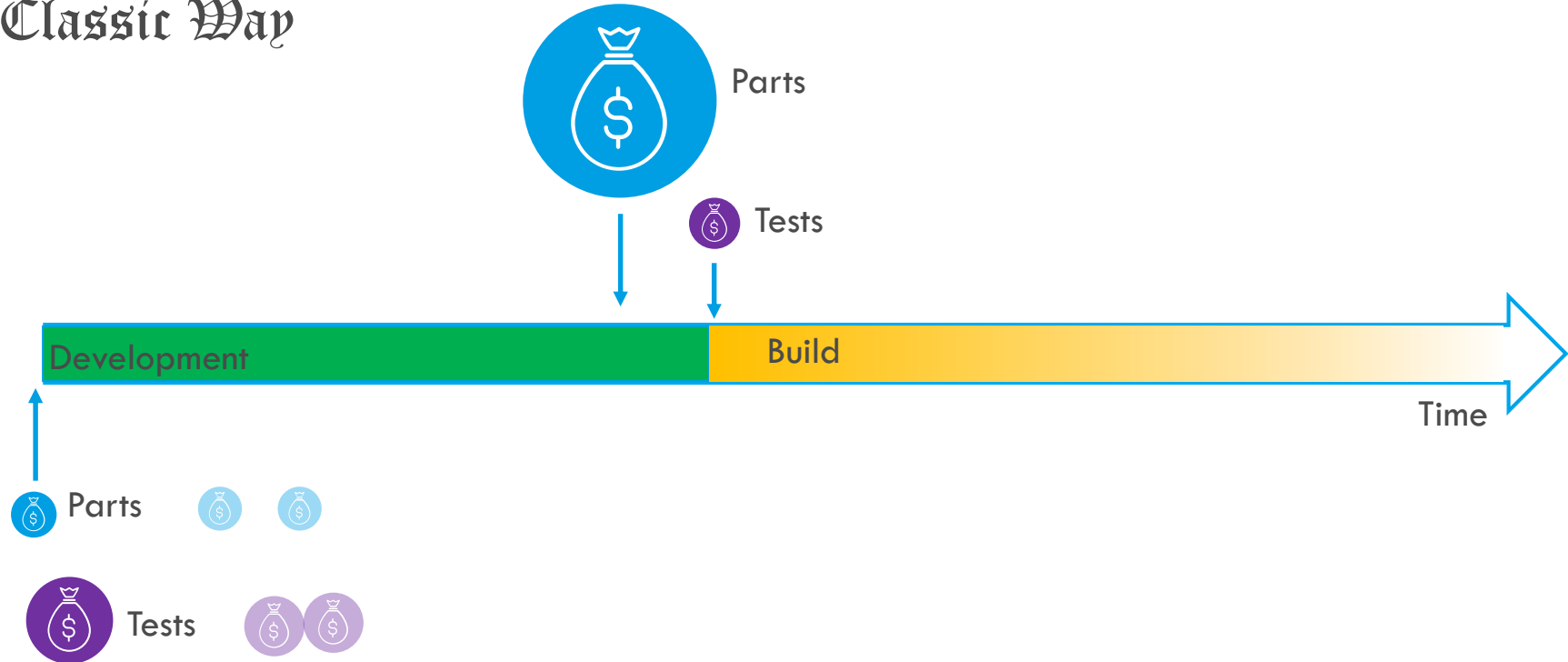


**BUILD** 

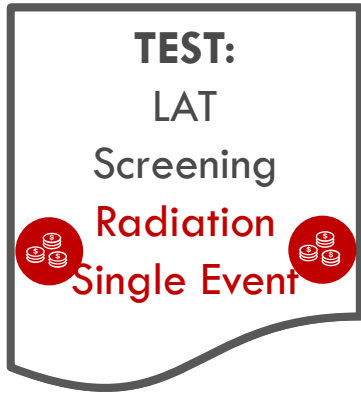
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- » Design release...








## Classic Way



## COTS Way

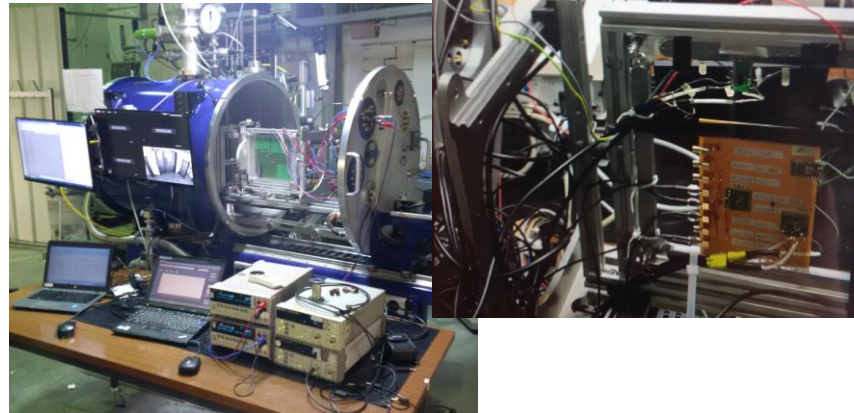


- » Check availability of parts
  - » Check the technology (CMOS, BiCMOS, bipolar)
  - » Define critical effects for the given device type (N-MOSFETs, OpAmps, voltage ref., ...)
  - » Check bias condition within application wrt. radiation sensitivity
  - » Reduce the number of different parts (e.g. MOSFETs)
  - » Define radiation source wrt. parts related critical parameters
- Examples:
- » MOSFET: Heavy ion
  - » FPGA: Mitigation techniques and/or laser SEE testing
  - » OpAmps: TID & SEE

- » Open devices with JetEtcher, Laser, chemical ablation, ... 
- » 1st step: Checking for destructive SEE by testing with Xe-ions (LET = 62.5 MeV/mg/cm<sup>2</sup>) 
- » Application conditions; not at (de-)rated bias voltage conditions 
  - » Regular coordination with development engineers 
- » Avoid to prepare detailed test plans
- » Avoid extensive test board design 
- » Do not prepare test reports for failed parts

Target: Optimized thru-put time, fast results

- » Test at UCL-HIF in Belgium  
  - » No transport issues
  - » Pre booking initialized
  - » Easy access to the device under test



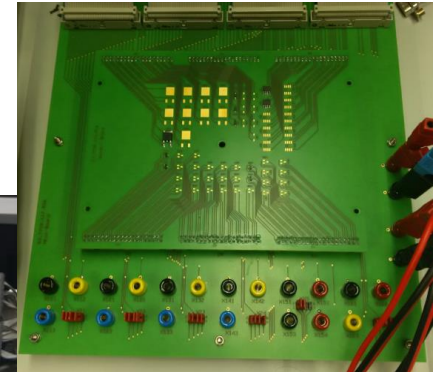
- » Fully automated measurement equipment developed to test different part types in parallel
- » Generic mother-board and part-specific daughter-board
- » Regular coordination with development engineers
  - » Checking for relevant parameters
  - » Apply application conditions
- » Avoid to prepare detailed test plans and test reports
- » Adapt daughter board for subsequent test campaigns



Target: Optimized thru-put time, fast results



- » Test at F-INT in Euskirchen
  - » No transport issues
  - » Pre booking initialized
  - » Test data will be provided w/o delay





- » Use of COTS parts require early interaction:  
EEE Parts Engineer ↔ Equipment Developer



- » EEE Parts cost are low  
But: Test costs may eat up savings  
→ Change Equipment Development Process to mitigate risks and save costs



- » Selection of parts & tests require experience
  - » Save Test costs
  - » Save development time (no redesign)



THANKS FOR YOUR ATTENTION

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