EMPC-2019 Technical Programme Schedule

Timing	Tuesday, 17th September			
09.00-09.20	Auditorium			
	Opening Session, Chair: Dr Luigi Calligarich			
	Welcome Address by President of IMAPS-Italy, Addresses by Presidents of IMAPS-Europe, IMAPS-North America			
09.20-10.50	Keynotes Session Chair: Prof. Nihal Sinnadurai, CEO ATTAC			
	Keynote – **Laurent Herard**, Group VP ST Microelectronics - Head of Back End Manufacturing & Technology R&D Keynote – **Prof. Marc Desmulliez**, School of Engineering & Physical Sciences, Institute of Sensors, Signals & Systems, Heriot Watt University, Scotland			
10.50-11.20	Tea / Coffee			
11.20-12.35	TuA1: MEMS	TuA2: Reliability & Quality 1	TuA3: Substrate Technologies	TuA4: Posters Set-up
	Audit A	**Audit B**	**Sala C**	**Poster Hall**
12.35-13.40	Networking & Lunch			
13.40-15.20	TuP1: Interconnection Technologies 1	TuP2: Reliability & Quality 2	TuP3: Manufacturing Technologies 1	TuP4: Poster Presentations
	Audit A	**Audit B**	**Sala C**	**Poster Hall**
15.20-15.50	Tea / Coffee			
15.50-17.30	TuP5: Interconnection Technologies 2	TuP6: Reliability & Quality 3	TuP7: Manufacturing Technologies 2	TuP8: Poster Presentations
	Audit A	**Audit B**	**Sala C**	**Poster Hall**
17.30 – 18.00	Exhibitor presentations	Exhibitor presentations	Exhibitor presentations	Commercial Poster Presentations
	Sponsors' Dinner – by Invitation			

Timing	Wednesday, 18th September			
09.00-10.30	Auditorium			
	Keynotes Session Chair:, Dr Luigi Calligarich, CEO Electron-Mec			
	Keynote – **Prof. Klaus-Dieter Lang,** Head of Fraunhofer Institute for Reliability and Microintegration IZM – Berlin, Germany Keynote – **Nan Wang**, Sr. Director, Technology and Quality, Supply Chain Operations, Cisco Systems, Inc.			
10.30-11.00	Tea / Coffee			
11.00-12.40	WA1: Interconnection Techs 3	WA2: Reliability & Quality 4	WA3: Manufacturing Technologies 3	WA4: Poster Set-up
	Audit A	**Audit B**	**Sala C**	**Poster Hall**
12.40-13.50	Networking & Lunch			
13.50-15.30	WP1: Interconnection Technologies 4	WP2: Reliability & Quality 5	WP3: Manufacturing Technologies 4	WP4: Poster Presentations
	Audit A	**Audit B**	**Sala C**	**Poster Hall**
15.30-16.00	Tea / Coffee			
16.00-17.40	WP5: Advanced Packaging for Electronics & Photonics 1	WP6: Power & Thermal Management	WP7: Green Electronics	WP8: Nano Technologies
	Audit A	**Audit B**	**Sala C**	**Sala D**
17.40-18.00	Exhibitor presentations	Exhibitor presentations	Exhibitor presentations	Exhibitor presentations
	Gala Dinner			

Timing	Thursday, 19th September			
09.00-10.30	**Auditorium**			
	Keynotes Session			
	Keynote **– Dr. Marco Romagnoli**, Head of Advanced Technologies for Photonic Integration at CNIT in Pisa, contract professor at Scuola Superiore S. Anna in Pisa, and former Director in R&D dept. - Pisa, Italy Keynote **– Dwight Howard,** APTIV, Manager, Electrical Engineering, North America & Asia-Pacific Product Development, Infotainment and Driver Information (IDI) PBU, Electronics & Safety Division			
10.30-11.00	Tea / Coffee			
11.00-12.40	ThA1: Advanced Packaging for Electronics & Photonics 2	ThA2: Modelling 1	ThA3: Applications	ThA4: Embedded Electronics & Smart Textiles
	Audit A	**Audit B**	**Sala C**	**Sala D**
12.40-13.20	Networking & Light Refreshments			
13.20-14.35	**Auditorium**			
	ThP1: Modelling 2			
	Chair: Prof. Knut Aasmundtveit, University of South-Eastern Norway			
14.35-15.15	**Closing Session & Awards**			
	Closing Session Chair: Prof. Nihal Sinnadurai, CEO ATTAC			
	Prof. Knut Aasmundtveit, 'Invitation to ESTC-2020, Norway', Dr. Daniel Nilsen-Wright, 'Invitation to EMPC-2021, Gothenburg, Sweden' EMPC-2019 Statistics Introducing the New IMAPS-Europe President Best Poster and Best Paper Awards Exhibition Raffle Awards (a bit of fun)			